



DIGITAL DESIGN ENGINEER F/M

NanoXplore is a pioneer in design and development of space-grade FPGA and e-FPGA located in Paris and Montpellier. As a rapidly growing company, NanoXplore is looking to continue building its software and hardware teams.

We are looking for a digital design engineer. The selected candidate will join our hardware team based in the Paris region. The mission is to develop different IPs on ASICs and FPGAs. Teamwork skills will be decisive in the selection of the candidate.

MISSIONS

Your different tasks will be:

- IP specifications and architecture definition.
- Realization of test plans.
- RTL coding / verification / code coverage.
- ATPG analysis and pattern generation.
- Writing of technical documents.

PROFILE

- University or Engineering School degree (Bac + 5 or Bac + 8).
- At least 5 years of industry or relevant experience.
- Knowledge of RTL languages: Verilog or VHDL.
- Knowledge of scripting languages (Python, Perl, Bash, etc.)
- Good communication skills in English and French.
- Adherence to deadlines.
- Having a tape-out experience is a plus.

COMPANY

Join our highly motivated team and be part of our success story “à la française,” in a company with a start-up mentality. We have always believed that our limits are drawn by your motivation. Always attentive to the wellbeing of its employees, NanoXplore allows you to grow and to develop your skills in a quick-paced, reactive working environment. Have any questions about what do we do? The Company? Daily life? Please contact us at careers@nanoxplore.com.

SUMMARY

- Contract: CDI – full-time.
- Location : SEVRES - Île-de-France.
- Experience: minimum 5 years of relevant experience.
- Salary: based on your profile.